



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Taylor R. Efland

Docket: TI-31678

Serial No.: 09/975,630

Art Unit: 2826

Filed: 10/12/01

Examiner: Andujar, L.

# For: Circuit Structure Integrating the Power Distribution Functions of Circuits and Leadframes into the Chip Surface

## **EXTENSION OF TIME**

February 24, 2003

Assistant Commissioner For Patents  
Washington, DC 20231

**MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)**

I hereby certify that on this date the above correspondence is being deposited with the U S Postal Service as First Class Mail in an envelope addressed to Assistant Commissioner for Patents, Washington, DC 20231.

Skrehot  
Michael K Skrehot Reg. No. 36 682

2-24-03  
Date

Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicants respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

<u>X</u>	One Month	(\$110)	<u>—</u>	Four Months	(\$1,450)
<u>—</u>	Two Months	(\$410)	<u>—</u>	Five Months	(\$1,970)
<u>—</u>	Three Months	(\$930)			

Any further necessary extension of time is hereby requested. Charge any and all fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. An original and two copies of this sheet are enclosed.

Respectfully submitted,

Wettest

Texas Instruments Incorporated  
Mail Station 3999  
P.O. Box 655474  
Dallas, TX 75265  
(972) 917-5653

Michael K. Skrehot  
Registration No. 36,682

RECEIVED